

**JABALPUR ENGINEERING COLLEGE, JABALPUR (MP)**  
**(An Autonomous Institute of Govt. of M.P. )**

**Affiliated to Rajiv Gandhi Technological University, Bhopal (MP)**  
**Scheme of Study and Examination (w.e.f. July 2010)**

**B.E. Third Year**

**Branch : Information Technology**

**Sem :Sixth**

B.E. Third Year										
Branch: Information Technology										
Semester: V										
Course Code	Subject	Periods			EVALUATION SCHEME					Credits
		L	T	P	SESSIONAL EXAM			ESE	SUB TOTAL	
					TA	CT	TOTAL			
IT-13	Automata and Compiler Design	3	1	-	10	20	30	70	100	4
IT-15	Microprocessor & Interfacing	3	1	-	10	20	30	70	100	4
IT-17	Internet & Web Technology	3	1	-	10	20	30	70	100	4
IT-19	Management information System	3	1	-	10	20	30	70	100	4
IT-20	E Commerce & Governance	3	1	-	10	20	30	70	100	4
(PRACTICAL/DRAWING/DESIGN)										
IT-14L	Automata and Compiler Design Lab	-	-	2	20	-	20	30	50	2
IT-16L	Microprocessor & Interfacing Lab	-	-	2	20	-	20	30	50	2
IT-18L	Internet & Web Technology Lab	-	-	2	20	-	20	30	50	2
IT-21L	Minor Project	-	-	2	20	-	20	30	50	2
IT-43L	Professional Activity			2	50	-	50	-	50	2
IT-44L	Seminar/Group Discussion			2	50	-	50	-	50	2
	Total	15	5	12	230	100	330	470	800	32

T.A. Teachers Assessment, CT- Class Test, ESE - End Semester Examination, Total Marks 800  
 Total Periods : 32, Total Credits : 32

## COURSE CONTENT & GRADE

(w.e.f. July 2010)

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	AUTOMATA AND COMPILER DESIGN	IT-13	Min “D”	Min “D”	5.0

### AUTOMATA AND COMPILER DESIGN

- Unit I:** Introduction: Alphabets, Strings and Languages; Automata and Grammars, Deterministic finite Automata (DFA)- Formal Definition, Simplified notation: State transition graph, Transition table, Language of DFA, Nondeterministic finite Automata (NFA), Equivalence of NFA and DFA, Minimization of Finite Automata, Regular expression, Regular Grammar, Regular languages, closure properties of Regular languages.
- Unit II:** Context free grammars, Properties of context free languages. Pushdown Automata: Non deterministic push down automata: Definition of a push down automata, the language accepted by a push down automata, Push down automata for context free languages, CFG’s for PDA, Deterministic Push down automata and Deterministic Context free languages.
- Unit III:** Compiler Structure: Compilers and Translators, Various Phases of Compiler. Lexical Analysis: The role of Lexical Analyzer, A simple approach to the design of Lexical Analyzer, Implementation of Lexical Analyzer. The Syntactic Specification of Programming Languages: CFG, Derivation and Parse tree, Ambiguity, Capabilities of CFG. Basic Parsing Techniques: Top-Down parsers with backtracking, Recursive Descent Parsers, Predictive Parsers, Bottom-up Parsers, Shift-Reduce Parsing, Operator Precedence Parsers, LR parsers (SLR, Canonical LR, LALR)
- Unit IV:** Intermediate Code Generation: Different Intermediate forms: three address code, Quadruples & Triples. Syntax Directed translation mechanism and attributed definition. Translation of Declaration, Assignment control flow, Boolean expression, Array References in arithmetic expressions, procedure calls, case statements, postfix translation.
- Unit V:** Run Time Memory Management: Static and Dynamic storage allocation, stack based memory allocation schemes, Symbol Table management. Code Optimization and Code Generation: Local optimization, Loop optimization, Peephole optimization, Basic blocks and flow graphs, DAG, Data flow analyzer, Machine Model, Order of evaluation, Register allocation and code selection

### References:-

- Louden, “Compiler construction”, Cengage learning .
- Alfred V Aho, Jeffrey D. Ullman, “Principles of Compiler Design”, Narosa.
- A.V. Aho, R. Sethi and J.D Ullman, “Compiler: principle, Techniques and Tools”, AW.
- Michal Sipser, “Theory of Computation”, Cengage learning.
- H.C. Holub, “Compiler Design in C”, Prentice Hall Inc.
- Hopcroft, Ullman, “Introduction to Automata Theory, Languages and Computation”, Pearson Education.
- K.L.P. Mishra and N.Chandrasekaran, “Theory of Computer Science : Automata, Languages and Computation”, PHI.

## COURSE CONTENT & GRADE

(w.e.f. July 2010)

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	MICROPROCESSOR AND INTERFACING	IT-15	Min "D"	Min "D"	5.0

### MICROPROCESSOR AND INTERFACING

#### Unit I

**Intel 8086 Microprocessor:** Introduction to 16-bit microprocessors, 8086 pin functions, Minimum and maximum mode operations. 8086 Architecture, register organization, addressing Modes, 8086 Memory banks and Memory organization, 8086 Instruction set and Assembly language programming.

#### Unit II

**Advanced microprocessors:** Salient features of advanced microprocessors. Review of evolution of advanced microprocessors: 186 / 286 / 386 / 486 / Pentium. Super scalar architecture of Pentium. 80286/386 Memory segmentation with descriptor tables, Privilege levels, Changing privilege levels, Paging including address translation, Page level protection, MMU, cache memory, Virtual memory.

#### Unit III

**I/O INTERFACING:** Introduction to the interfacing chips 8255. Interfacing keyboards, printers, LEDS with Intel 8086 Microprocessor. Interfacing of 8254 programmable interval timer, 8259A Programmable interrupt controller & 8257 DMA controller with Intel 8086 Microprocessor.

#### Unit IV

**Memory Interfacing:** Interfacing of RAM and ROM with Intel 8086 Microprocessor.

**Serial communication interface:** RS 232C standards, Interfacing of USART chip 8251 with Intel 8086 Microprocessor.

#### Unit V

**Microcontroller:** Introduction to micro controller 8051, its architecture, Register set, operational features, pin description, I/O configuration, interrupts, addressing modes, an overview of 8051 instruction set.

#### Books

1. B.B. Brey (PHI), "The Intel Microprocessors, Architecture, Programming and Interfacing".
2. A Triebel & Avtar Singh (PHI), "The 8088 & 8086 Microprocessor".
3. D. Hall (Mc-Graw Hill), "Advanced Microprocessor and Interfacing".
4. A. Pal (TME), "Microprocessors Principles & Applications".
5. A.P. Mathur (TMA), "Introduction to Microprocessors". Intel Corporation Microprocessors Data manuals.
6. Microprocessor Training Inc., "Microprocessor Fundamentals & Applications (Handson)".

## COURSE CONTENT & GRADE

(w.e.f. July 2010)

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	INTERNET AND WEB TECHNOLOGY	IT-17	Min “D”	Min “D”	5.0

### INTERNET AND WEB TECHNOLOGY

**Unit-I :** An Introduction to Web Engineering, History of web Development, Time line, Motivation, Categories of Web Applications, Characteristics of Web Applications. Evolution and Need for Web Engineering, Web Engineering Models, Software Engineering v/s Web Engineering. Introduction to Browser and search engines, Search fundamentals, Search strategies, Directories search engines and Meta search engines, Working of the search engines, Miscellaneous Web Browser details.

**Unit-II :** Introduction to Web Servers: Features of web servers, caching, case study-IIS, Apache, Configuring web servers.

**Unit-III :** Technologies for Web Applications: HTML and DHTML, HTML Basic Concepts, Static and dynamic HTML, Structure of HTML documents, HTML Elements, Linking in HTML, Anchor Attributes, Image Maps, Meta Information, Image Preliminaries, Layouts, Backgrounds, Colors and Text, Fonts, Tables, Frames and layers.

Database integration, CSS, Positioning with Style sheets. Introduction to JAVA SCRIPT, Cookies Creating and Reading Cookies

Technologies for Web Applications: Introduction of XML, Validation of XML documents, DTD, Ways to use XML, XML for data files, HTML Vs XML, Embedding XML into HTML documents, Converting XML to HTML for Display, Displaying XML using CSS and XSL, Rewriting HTML as XML.

**Unit-IV : Creating Cohesive Websites:** Conceptual Overview of website Development, Website Design issues, Conceptual Design, High-Level Design, Indexing the Right Stuff, Grouping Content. Architectural Page Mockups, Design Sketches, Navigation Systems. Searching Systems Good & bad web design, Process of Web Publishing. Phases of Web Site development, enhancing your web-site, submission of website to search engines. Web security issues, security audit of websites, Web effort estimation, Productivity, Measurement, Quality usability and reliability.

**UNIT-V : Requirements Engineering for Web Applications:** Introduction, Fundamentals, Requirement Source, Type, Notations Tools. Principles Requirements Engineering Activities, Adapting RE Methods to Web Application.

Introduction to http and https, http vs. https, Dynamic Web Content, Introduction of ASP.Net, PHP, Database connectivity (MySQL/Oracle)

### TERM WORK

1. At least ten practical experiments based on above syllabus and a mini project is desirable to be completed by a group of three that cover following tools.

• HTML DHTML XML Java Script

### Recommended Books:

1. Roger S.Pressman, David Lowe, “Web Engineering”, Tata Mcgraw Hill Publication, 2007
2. Achyut S Godbole and Atul Kahate, “Web Technologies”, Tata McGraw Hill
3. Gopalan N P, Akilandeswari “Web Technology: A Developer s Perspective” , PHI
4. NEIL GRAY “Web server Programming” Wiley
5. CHRIS BATES Web Programming: Building Internet applications Wiley
6. Moller, “An Introduction to XML and Web Technologies” , Pearson Education New Delhi, 2009
7. Beginning XML 4th Edition Hnter, Refter, Fawset Wiley India
8. Internet & World Wide Web How to Program, Pearson education, 3rd edition, by: H.M. Deitel, P.J. Deitel, A.B. Goldberg.
9. C. Xavier, “Web Technology & Design ”, Tata McGraw Hill. 10 Ivan Bay Ross, “HTML, DHTML, Java script, Perl CGI”, BPB

## COURSE CONTENT & GRADE

(w.e.f. July 2010)

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	MANAGEMENT INFORMATION SYSTEM	IT-19	Min “D”	Min “D”	5.0

### MANAGEMENT INFORMATION SYSTEM

#### UNIT I INFORMATION SYSTEM AND ORGANIZATION

Matching the Information System Plan to the Organizational Strategic Plan – Identifying. Key Organizational Objective and Processes and Developing an Information System. Development – User role in Systems Development Process – Maintainability and Recoverability in System Design.

#### UNIT II REPRESENTATION AND ANALYSIS OF SYSTEM STRUCTURE

Models for Representing Systems: Mathematical, Graphical and Hierarchical (Organization Chart, Tree Diagram) – Information Flow – Process Flow – Methods and Heuristics – Decomposition and Aggregation – Information Architecture – Application of System Representation to Case Studies.

#### UNIT III SYSTEMS, INFORMATION AND DECISION THEORY

Information Theory – Information Content and Redundancy – Classification and Compression – Summarizing and Filtering – Inferences and Uncertainty – Identifying Information needed to Support Decision Making – Human Factors – Problem characteristics and Information System Capabilities in Decision Making. Decision Support Systems for Business Intelligence, Case Study- Business Intelligence tools i.e. **IBM Cognos**, [Microsoft Business Intelligence](#) tools.

#### UNIT IV INFORMATION SYSTEM APPLICATION

Transaction Processing Applications – Basic Accounting Application – Applications for Budgeting and Planning – Other use of Information Technology: Automation – Word Processing – Electronic Mail – Evaluation Remote Conferencing and Graphics – System and Selection – Cost Benefit – Centralized versus Decentralized Allocation Mechanism.

#### UNIT V DEVELOPMENT AND MAINTENANCE OF INFORMATION SYSTEMS

Systems analysis and design – System development life cycle – Limitation – End User Development – Managing End Users – off-the shelf software packages – Outsourcing – Comparison of different methodologies.

#### TEXT BOOK:

1. Laudon K.C, Laudon J.P, Brabston M.E, “Management Information Systems - Managing the digital firm”, Pearson Education, 2004.

#### REFERENCES:

1. Turban E.F, Potter R.E, “Introduction to Information Technology”; Wiley, 2004.
2. Jeffrey A.Hoffer, Joey F.George, Joseph S. Valachich, “Modern Systems Analysis and Design”, Third Edition, Prentice Hall, 2002.

## COURSE CONTENT & GRADE

(w.e.f. July 2010)

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	E-COMMERCE AND GOVERNANCE	IT-20	Min “D”	Min “D”	5.0

### E-COMMERCE AND GOVERNANCE

- Unit I:** Introduction to e-commerce: History of e-commerce, e-business models B2B, B2C, C2C, C2B, legal; environment of e-commerce, ethical issues, electronic data interchange, value chain and supply chain, advantages and disadvantages of e-commerce.
- Unit II:** Electronic Payment Systems: Credit cards, debit cards, smart cards, e-credit accounts, e-money, Marketing on the web, marketing strategies, advertising on the web, customer service and support, introduction to m-commerce, case study: e-commerce in passenger air transport.
- Unit III:** E-Government, theoretical background of e-governance, issues in e-governance applications, evolution of e-governance, its scope and content, benefits and reasons for the introduction of e-governance, e-governance models- broadcasting, critical flow, comparative analysis, mobilization and lobbying, interactive services / G2C2G.
- Unit IV:** E-readiness, e-government readiness, E- Framework, step & issues, application of data warehousing and data mining in e-government, Case studies: NICNET-role of nation wide networking in e-governance, e-seva.
- Unit V:** E-Government systems security: Challenges and approach to e-government security, security concern in e-commerce, security for server computers, communication channel security, security for client computers.

### References:-

- Gary P. Schneider, “E-commerce”, Cengage Learning India.
- C.S.R. Prabhu, “E-governance: concept and case study”, PHI Learning Private Limited.
- V. Rajaraman, “Essentials of E-Commerce Technology”, PHI Learning Private Limited.
- David Whiteley, “E-commerce study , technology and applications”, TMH.
- J. Satyanarayan, “E-government: The science of the possible”, PHI Learning Private Limited.
- P.T. Joseph, “E-Commerce An Indian Perspective”, PHI Learning Private Limited.
- Hanson and Kalyanam, “E-Commerce and Web Marketing”, Cengage Learning India.

**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	AUTOMATA AND COMPLIER DESIGN LAB	IT- 14L	Min “D”	Min “D”	5.0

**AUTOMATA AND COMPLIER DESIGN LAB****LIST OF EXPERIMENTS :**

1. Develop a lexical analyzer to recognize a few patterns.
2. Write a programme to parse using Brute force technique of Topdown parsing.
3. Develop LL (1) parser (Construct parse table also).
4. Develop an operator precedence parser (Construct parse table also)
5. Develop a recursive descent parser
6. Write a program for generating for various intermediate code forms
  - i) Three address code ii) Polish notation
7. Write a program to simulate Heap storage allocation strategy
8. Generate Lexical analyzer using LEX
9. Generate YACC specification for a few s
10. Given any intermediate code form implement code optimization techniques
11. Study of an Object Oriented Compiler.

**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

Branch	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	MICROPROCESSOR & INTERFACING LAB	IT-16L	Min "D"	Min "D"	5.0

**MICROPROCESSOR & INTERFACING LAB****LIST OF EXPERIMENTS :**

1. Add a data byte located at offset 0500H in 2000H segment to another data byte available at 06000H in same segment and store the resulting 0700H in same segment?
2. Add the contents of memory location 2000H, offset 0500H to the contained of accumulator.
3. Write a program to find the average to two temperature name HI-TEMP and LOTEMP and puts the result in the memory location AV-TEMP.
4. Find out the largest number from an unordered array of sixteen 8-bit numbers stored sequentially in the memory locations starting at offset 0500H in the segment 2000H
5. Move a byte string, 16 bytes long, from the offset 0200H to 0300H1. Add a data byte located at offset 0500H in 2000H segment to another data by teavailable at 06000H in same segment and store the resulting 0700H in same segment?
6. Write a program to add a profit factor to each element in a cost array and puts the result in a PRICES array, where profit factor is 15H and COST =20H, 28H, 15H, 26H, 19H, 27H, 16H, 29H.
7. Write a program to find out the number of positive numbers and negative numbers from a given series of signed numbers.
8. Write a program that performs the addition, subtraction, multiplications, division of the given operands. Perform BCD operation for addition and subtraction.
9. A Program to find out the number of even and odd numbers from a given series of 16 bit hexad 4ecimal numbers.
10. write a program to implement byte multiplication.
11. write a program to implement word multiplication.
12. write a program for packed BCD from ASCII.
13. write a program to implement BCD multiplication.
14. write a program to implement BCD division.
15. write a program to implement BCD subtraction.
16. write a program to convert signed byte to word.
17. write a program to implement scan string for character.
18. write a program to implement IF THEN ELSE statements.
19. write a program to convert BCD to HEX(resister parameter).



**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

<b>Branch</b>	<b>Subject Title</b>	<b>Subject Code</b>	<b>Grade for End Sem</b>		<b>CGPA at the end of every even semester</b>
			<b>T</b>	<b>P</b>	
<b>BE</b>	<b>INTERNET &amp; WEB TECHNOLOGY LAB</b>	<b>IT-18L</b>	Min “D”	Min “D”	5.0

**INTERNET & WEB TECHNOLOGY LAB****LIST OF EXPERIMENTS :**

1. At least ten practical experiments based on above syllabus and a mini project is desirable to be completed by a group of three that cover following tools.
  - HTML
  - DHTML
  - PHP
  - XML
  - Java Script, CGI, PERL
  - ASP

**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

<b>Branch</b>	<b>Subject Title</b>	<b>Subject Code</b>	<b>Grade for End Sem</b>		<b>CGPA at the end of every even semester</b>
			<b>T</b>	<b>P</b>	
BE	MINOR PROJECT	IT-21L	Min “D”	Min “D”	5.0

**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

Course	Subject Title	Subject Code	Grade for End Sem		CGPA at the end of every even semester
			T	P	
BE	<b>PROFESSIONAL ACTIVITY</b>	IT-43L	Min “D”	Min “D”	5.0

**PROFESSIONAL ACTIVITY  
(Suggested Exercise)**

- Student shall visit a nearby Industry and shall prepare a technical report suggesting some improvement in operation.
- Student shall Design and fabricate a new laboratory equipment. He shall prepare a design report.
- Student shall improve an existing lab equipment and prepare chart or lab manual .
- Student shall publish a review paper in some Indian Journal.
- Student shall make a report on an Industry employing latest technology/ Innovation.
- Student shall prepare a working model of a machine part.
- Student shall make a software/ comp. program for the Institute to enhance efficiency in its working.
- Student shall prepare a detailed project report to start a small-medium enterprise.
- A group of student shall register with the Industry cell and submit a report on work done there about Institute-Industry linkage.
- Experimental work on a new set of equipments.
- Seminar Presentation with a report submitted to the supervisor.

**Note** : The list of activities can be modified as per requirements of the department.

A hand written report of about 30 pages duly signed by the student and the concerned teacher should be submitted.

**COURSE CONTENT & GRADE****(w.e.f. July 2010)**

<b>Course</b>	<b>Subject Title</b>	<b>Subject Code</b>	<b>Grade for End Sem</b>		<b>CGPA at the end of every even semester</b>
			<b>T</b>	<b>P</b>	
BE	<b>SEMINAR/GROUP DISCUSSION</b>	IT-44L	Min “D”	Min “D”	5.0

**Objectives of Group Discussion & Seminar** is to improve the Mass Communication and Convincing/ understanding skills of students and it is to give student an opportunity to exercise their rights to express themselves.

**Evaluation** will be done by assigned faculty based on group discussion and power point presentation.